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(HE)
PATENT
8733.126.00
7/12/01

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re application of

Duck-Kyun CHOI

Group Art Unit: 2823

Application No.: 09/170,625

Examiner: G. Fourson

Filed: October 13, 1998

For: **METHOD FOR FABRICATING A THIN FILM TRANSISTOR**

AMENDMENT UNDER 37 C.F.R. § 1.116

COMMISSIONER FOR PATENTS
WASHINGTON, D.C. 20231

SIR:

*do not
enter
5/8/12/01*

In response to the final Office Action dated February 2, 2001, the time period for response being extended two months to July 2, 2001, Applicant requests that the application be amended as follows:

IN THE CLAIMS:

Please amend claims 1, 14 and 25 as follows.

- Pub
C-01*
1. A method of fabricating a thin film transistor for a liquid crystal display having a plurality of pixels comprising:
- forming an amorphous silicon layer as an active layer on a glass substrate;
 - forming a gate insulating layer and a gate electrode on the amorphous silicon layer;
 - doping impurities of a first conductive type in the amorphous silicon layer;
 - forming a metal layer on the exposed portions of the amorphous silicon layer; and